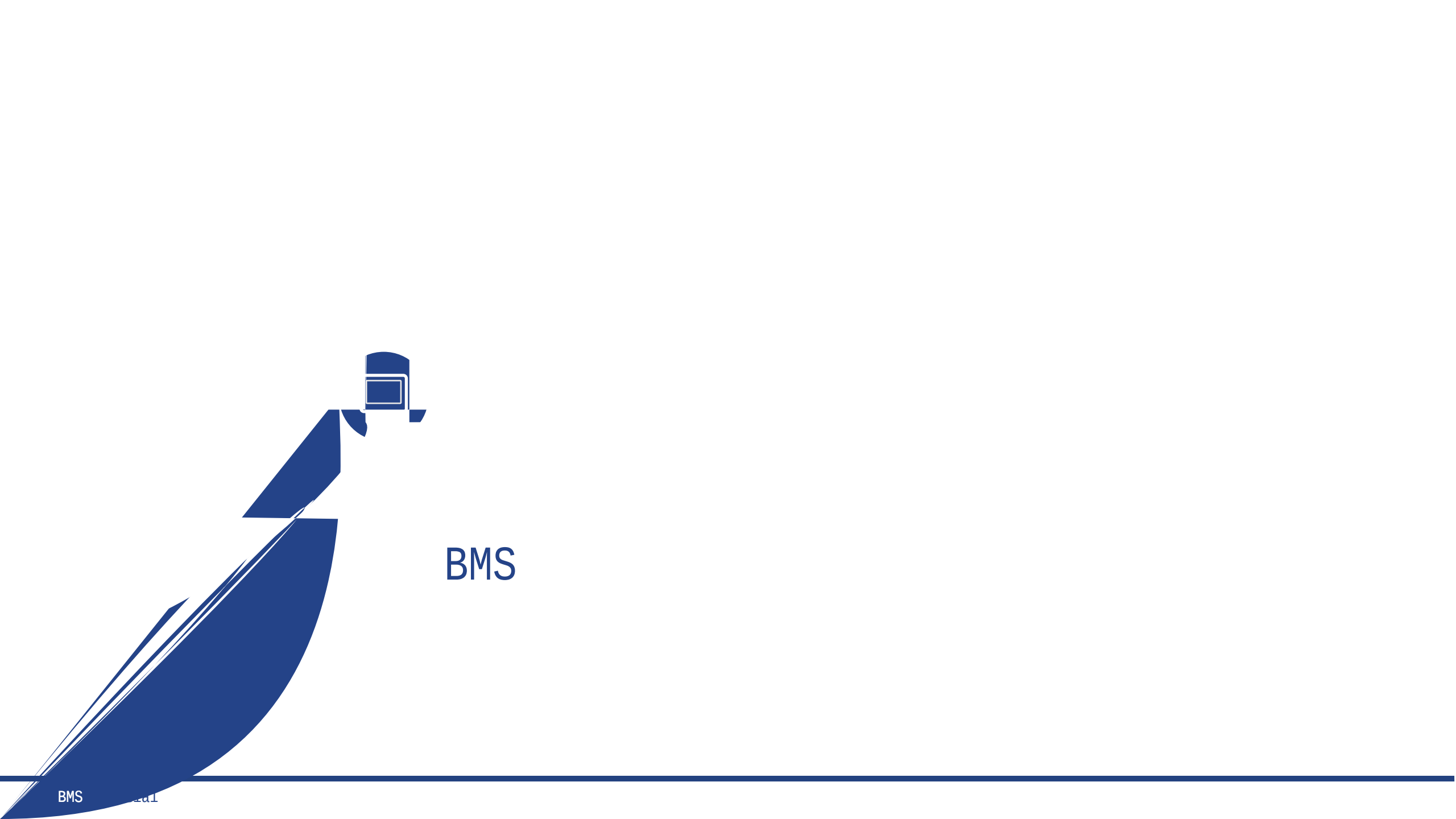
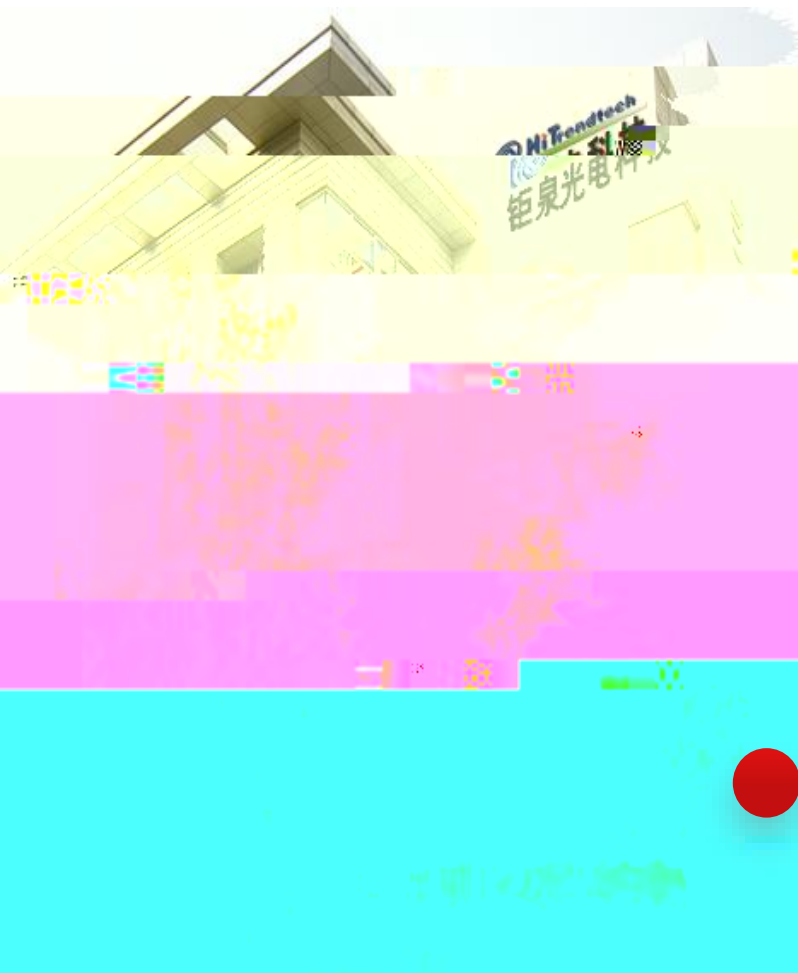


()

2024 3







2022.09 0 K⁻ @ Ĩ : 2

2019.08 Aî0û ‡ Ü 6 œ (

2019.03 Aî0û :#§ d\$_ € œ (-AÑGÿ ° _] ó

2005.05 œ (Aî0û ¾ :#§ P"•

—

()



. Aö × s	(æ 1	Gÿ
C Y	" , s	N©
	+cB']	N©
Lö @+eD 3 .AîAÑ C 9 s	",«Aà	N©
AÑ1Ç jEÿ &:‡ Æ s	" , s	N©
v 7	- µ , s	N©
	#§ F , s	N©

] - z , f>| J • JLö @+eD 6 J*6 » ... }
 :#SLö @+eD >| J • J*6 » ... }
] - ~ >~>| J • J *6 » ... }
 Lö @+eDK à ° _ 6,- *6 » ... }

C JAÔAñ

Ö CXGÿ1Ñ*6 f3+AÔAñ
 1 J. Aö × s1Ñ*6?ô93

ò >>| J 7 ö f Ê

- æ 7 ö
 - æ 7 ö
 - æ 7 ö

产品中心



泓江立口IC



江蘇省人口



Hitrendtech

2023

œ (CXGÿ1Ñ*6 f3+ ISO9001:2015 7 ö
, '2î/Ž ¼?±"r , ¾2012 α 12 8 18 OÆ!QFJE÷
TUVAÔAñ Ä

*0ûCXGÿ1Ñ*6 f3+,-, ' È _ - œ (,'CX
Gÿ#k Ø 9 ¿ Ã6|CS >.ž È C ¾Aö [%œ X ¼ Î
LuCXGÿL NÈ È 15 iF È 1 | iF œ (,'
5 x x5 N® Ò | Ç Ct, 'Añ ž È 9ç Ç1 ¹
H ï Ä

œ (,'CXGÿ1Ñ*6 f3+E÷0; k?±+a 1Ñ*66|
CS Ã Ct\$À È Ã >N® Ò 9 £,'E÷
0; Ã AîAÑ ¼ 0 E÷0; Ã G÷C]E÷0; Ã
+O x1Ñ*6E÷0; Ã #{Gÿ 6 À iF E÷0;
1y4ô @ È)F ÈE÷0; *0û-(£ · & È |.ž
9ç Ç õ?±, ' Y ¼Ct\$À ÈF >|- ?ö Ã#{Gÿ ¼
6 À È.ž CXGÿ f3+ 9 xF >| | 15 iF Ä



Ability 7- È -- Î »"r _ , = Ý w.D 7- È

Superior N¶ x--2î,ú"r2î È 0 N¶ x F+|,´ H/ð × ñ

Innovation K à -- ÷ ¾ M™ È6Š'V8²7- ÿ Â7-\$À Ä+X

Collaboration • Æ--5Ô1†5Ô È È Q Ò g% ? Ö È ; K È ÄK.,ø Y JC' à pM'

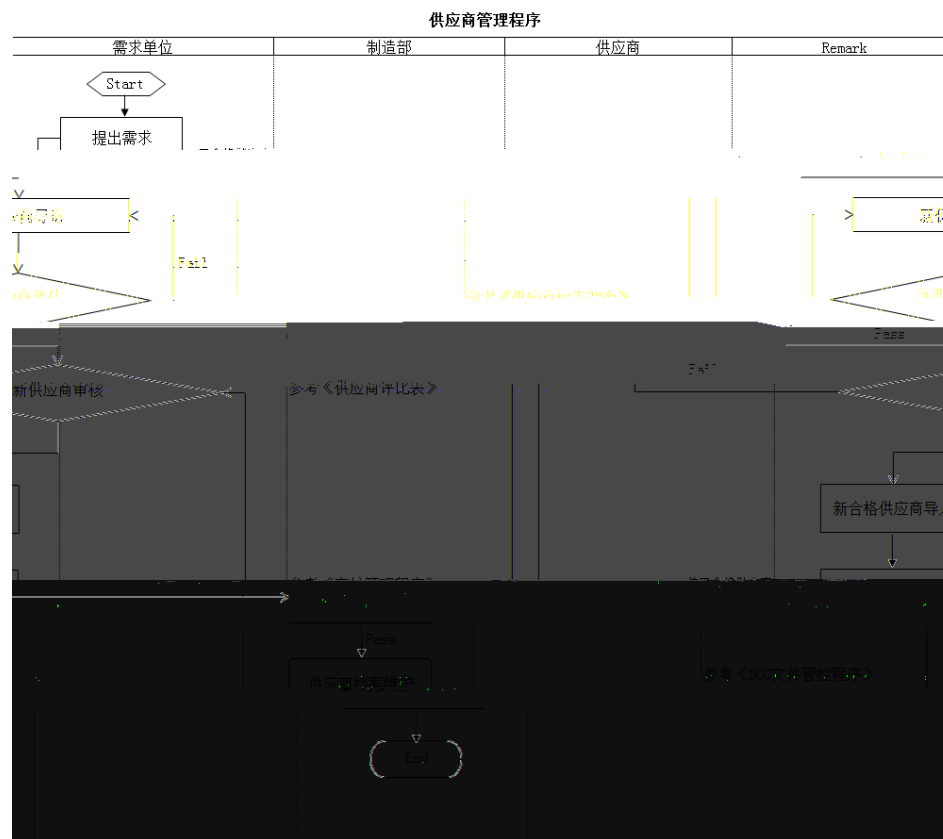
Ë Ä v!ÿ ¢ ÖF >| O!Q J5 Aô ' 6|CS Ê y

职能		职责定义
制造部	制造部主管	签核《供应商评比表》--签核及价格评比
	委外生管单位	提交《供应商评比表》--交期、配合度部分评比
	测试单位	提交《供应商评比表》--测试部分评比
	产品工程单位	提交《供应商评比表》--产品部分评比
	质量单位	提交《供应商评比表》--质量部分评比

Aô" 7 ö

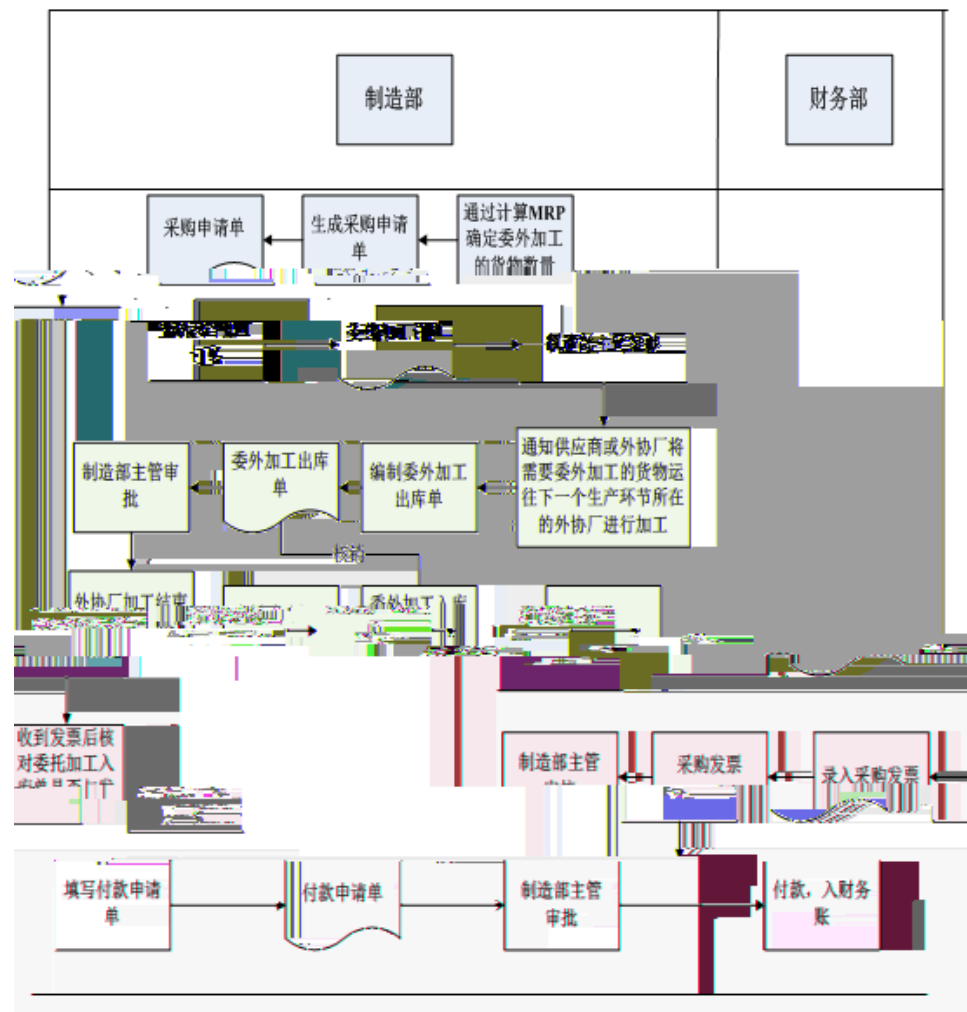
分数	90~100	70~89	60~69	59以下
等级	A	B	C	D
奖惩	优先并增加采购量	维持正常采购量	减少采购量,并连续三次加强督导	连续三次要求供应商提供,即淘汰该供应商

由于某些材料属特殊制程,或只有单一厂商生产,则评比结果不被等级范围限制;结果仍需通知厂商,要求改善达到标准。



+O x Ú)ß 7 ö Æ J#q0; Ö

U> § ERP3+5 Œ j £ È 4 ¿ 6 ×
 ñ 1#{,´-(£ ÿ Ä » ' È(x \ È ©!Q È
 | 6 ' È+O × ~.1 È f É &L\$1y ÅE-
 • |] È 9'— ©!Q ' ú(x \ ' ® pM0
 ÿ È ½5 8 4 ¿ F 5 Ð Aà ... È £
 Î)à ¶ × ñ,´ F-\$ß W Ä



2« »	Ë Ä v	- xFJFf	- x µ é	- xNÁ)·
! 6 ²	¼8`0 °	1. http://www.hjtc.com.cn/ 2. ftp://hjftp.hjtc.com.cn	1. WIP Äwork in product Ä 2. +O x ž PEÄ	!ÿ Y : x 8&é } J mail,' '? Ö z!ÿ(w ! 6,'+O xF Ö Ä
1>ö ²	~ Y0 °	http://www.TSHTKJ.com	WIP Ä Ä ^	Î & B
	K⁻+e0 °	http://ftp.ksoft.com.cn/login/	WIP Ä Ä ^	Î & B
	ü FJ	http://wip.tfme.com:8080/admin/login.aspx	WIP Ä Ä ^	!ÿ Y : x 9&é z } J mail' '? Ö z!ÿ © x ñ,'+O xF Ö Ä
# {B ²	ÜL¶0 °	1. KLT_LOG_REPORT2@kltech.com.cn 2. 218.104.45.92	1. WIP Ä Ä ^ 2. +O x ž PEÄ Ä datalog Ä mapping ÄBin data Ä	!ÿ Y : x 9&é z } J mail' '? Ö z!ÿ © x ñ,'+O xF Ö Ä !ÿ Y Š : J 6 f Y+O x ž : P ` FTP Ä
	¹#{ z , f	1.Mail 2.FTP	1. WIP Ä Ä ^ 2. +O x ž PEÄ Ä datalog Ä mapping ÄBin data Ä	!ÿ Y : x 9&é z } J mail' '? Ö z!ÿ © x ñ,'+O xF Ö Ä

质量监控指标				
项目	指标	监控频度	备注	监控形式
wafer 良率	95%~99%	每批	视产品而定	代工月报
封装良率	0.99	每批		代工月报
封装工艺	CPK>1.33	每月		代工月报
封装可靠性	零失效	季度		
CP测试良率	95%~99%	每批	视产品而定	代工月报
95%~98%	每批	视产品而定	代工月报	FT测试良率
BQC 抽检标准	AOQL=0.065%	每批		异常反馈
外观检验标准	1	每批		异常反馈
Loss+Damage	<10ea	每批		异常反馈

- 1 CPK Complex Process Capability index ,
CPK
A++
A+ 2.0 Cpk
A 1.67 Cpk



f F	× ñ2« »	İ7- ýF		



Features:

Battery monitor : 3~6 series cells

16bit ADC*2 : simultaneous current & voltage sampling

accuracy of cell voltage <5mV @Full Range

accuracy of cell current < 0.2% @Full Range

Integrated : LDOs 3.3/5V, Pre-Charge, Pre-Discharge

High voltage tolerance : High supply voltage absolute maximum
(up to 36 V)

Support Auto or host controlled cell balancing & protections

Power consumption:

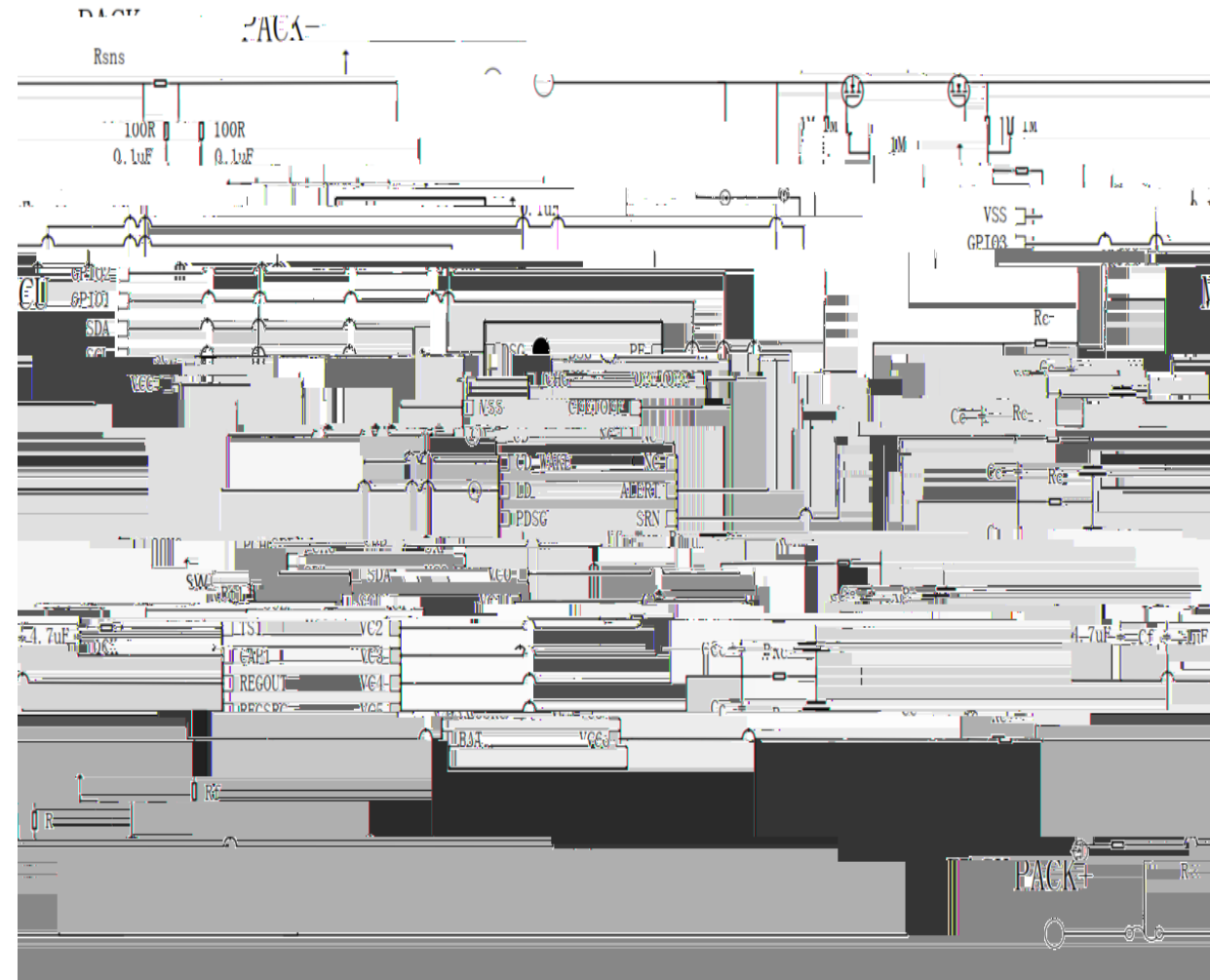
Shut down mode: <2uA

Support : I2C with CRC

Others:

Package: DFN30

Operation Temperature: -40 ~+85



Features:

Battery monitor : 6~12 series cells

16bit ADC*2 : simultaneous current & voltage sampling

accuracy of cell voltage <5mV @Full Range

accuracy of cell current < 0.2% @Full Range

Integrated : LDOs 3.3/5V, Pre-Charge, Pre-Discharge

High voltage tolerance : High supply voltage absolute maximum
(up to 72 V)

Support "Auto or host controlled cell balancing" protections

Power consumption:

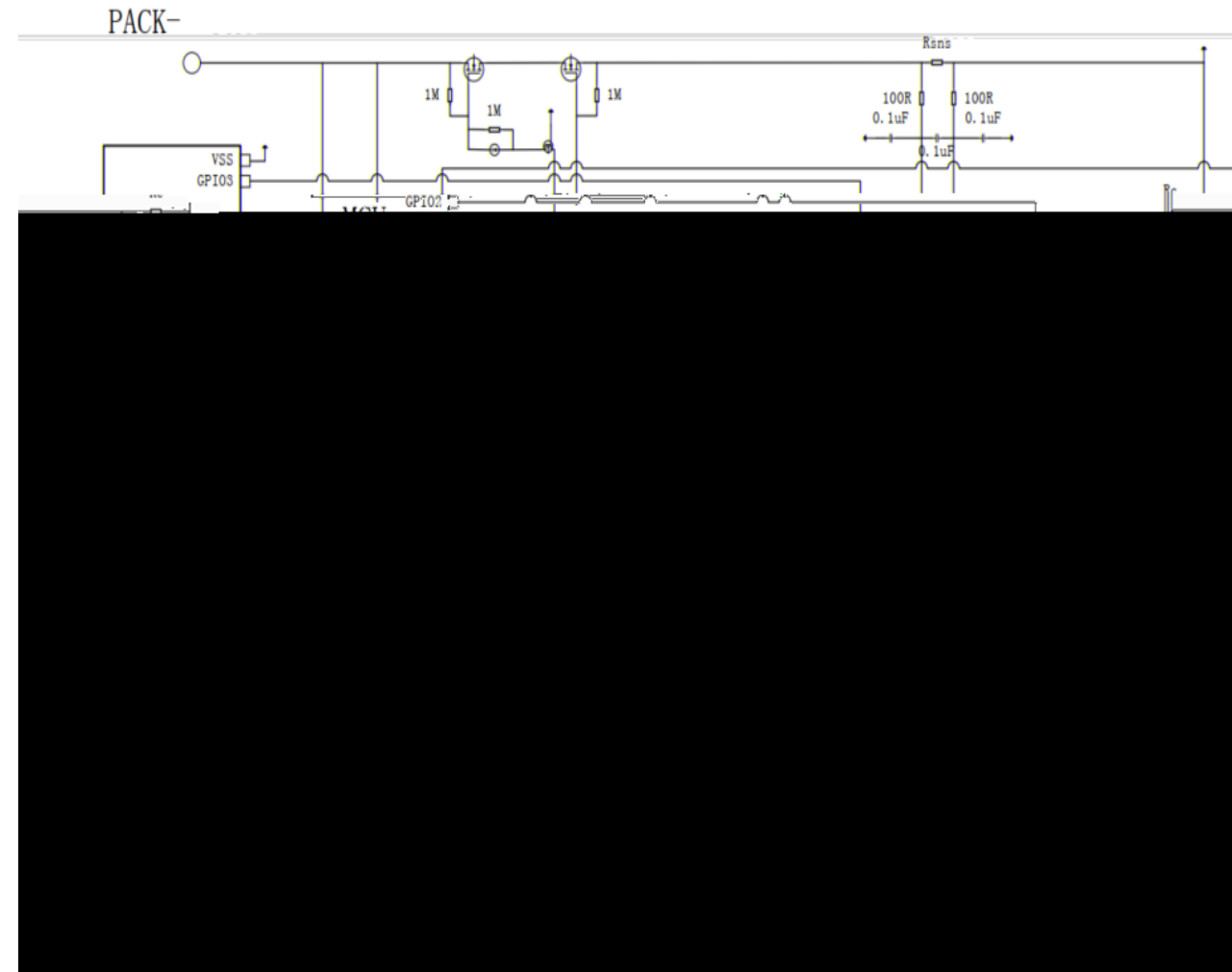
Shut down mode: <2uA

Support : I2C with CRC

Others:

Package: DFN44

Operation Temperature: -40 ~+85



Features:

Battery monitor : 9~18 series cells

16bit ADC*2 : simultaneous current & voltage sampling

accuracy of cell voltage <5mV @Full Range

accuracy of cell current < 0.2% @Full Range

Integrated : LDOs 3.3/5V, Pre-Charge, Pre-Discharge

High voltage tolerance : High supply voltage absolute maximum
(up to 108 V)

Support ÒAuto or host controlled cell balancing Èprotections

Power consumption:

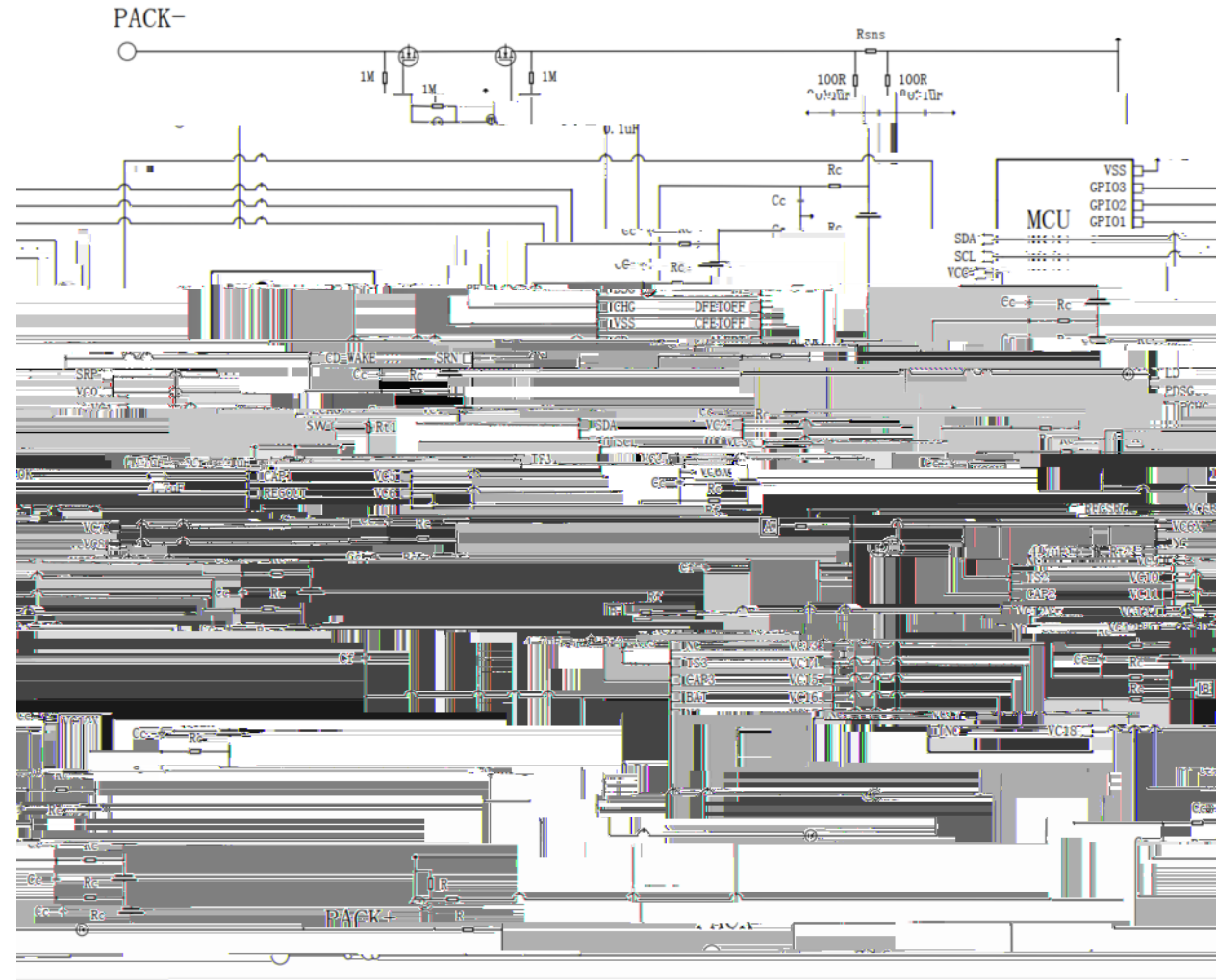
Shut down mode: <2uA

Support : I2C with CRC

Others:

Package: DFN54

Operation Temperature: -40 ~+85

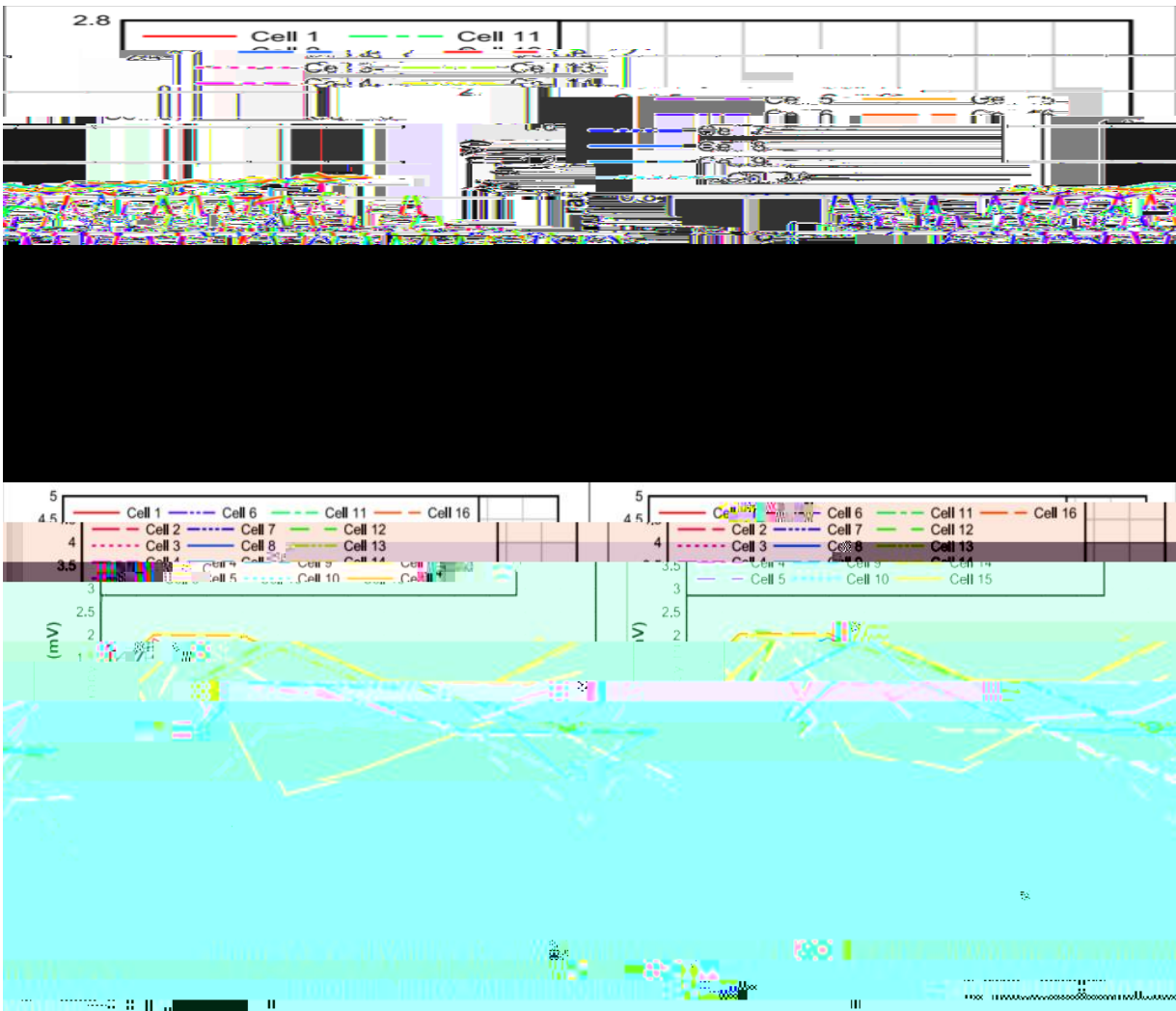
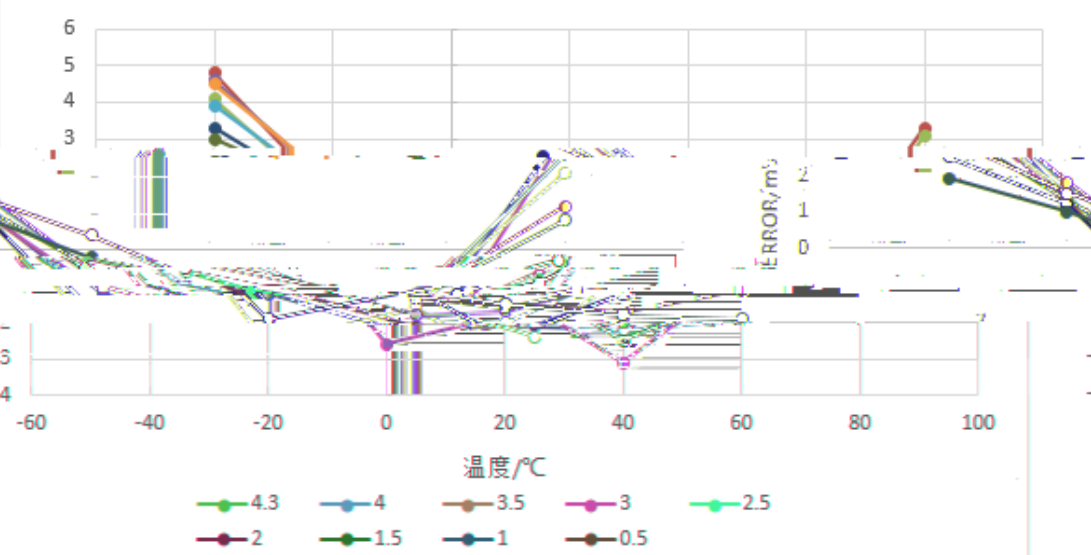


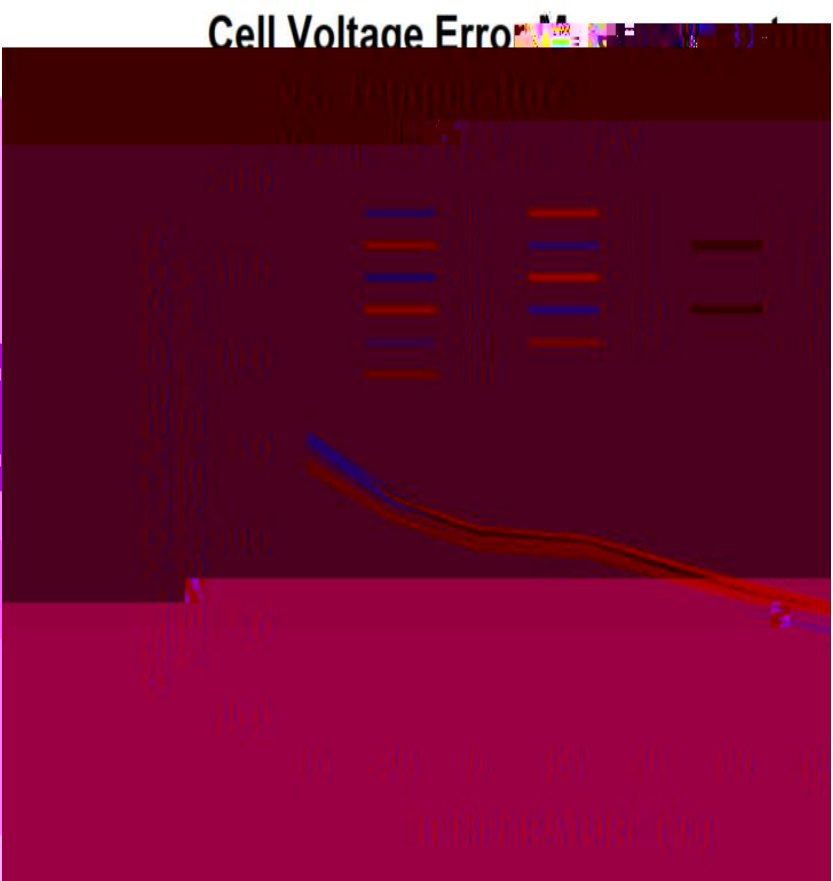
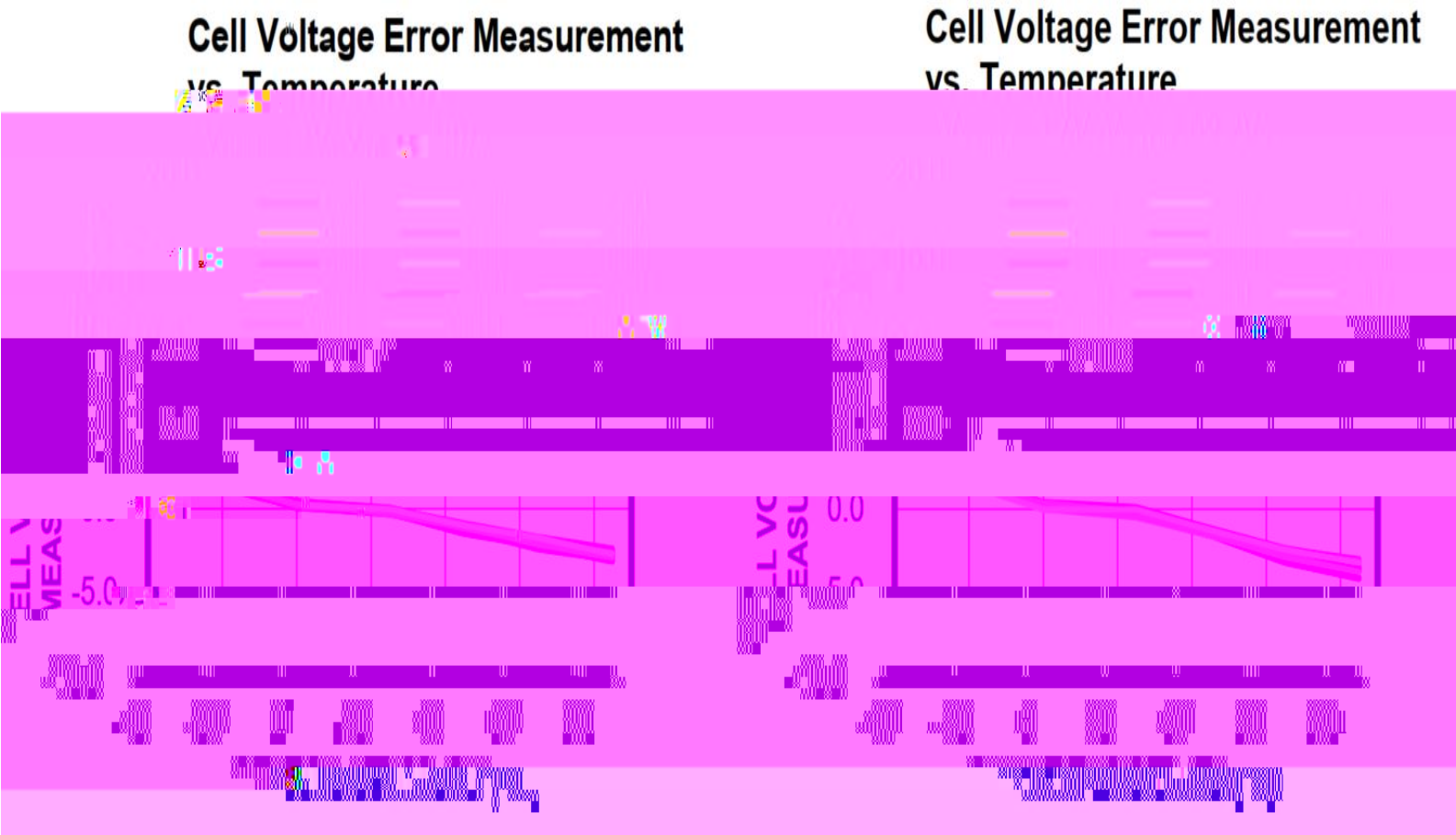
	HT32106	HT32112	HT32118
	3~6	6~12	9~18
	MCU	MCU	MCU
	16 Bit VADC	16 Bit VADC	16 Bit VADC
	16 Bit CADC	16 Bit CADC	16 Bit CADC
	√	√	√
	√	√	√
	√	√	√
	√	√	√
/	√	√	√
/	√	√	√
LDO 3.3V/5V	√	√	√
(I C)	√	√	√
	√	√	√
	DFN30	DFN44	DFN54
	-40 ~85	-40 ~85	-40 ~85

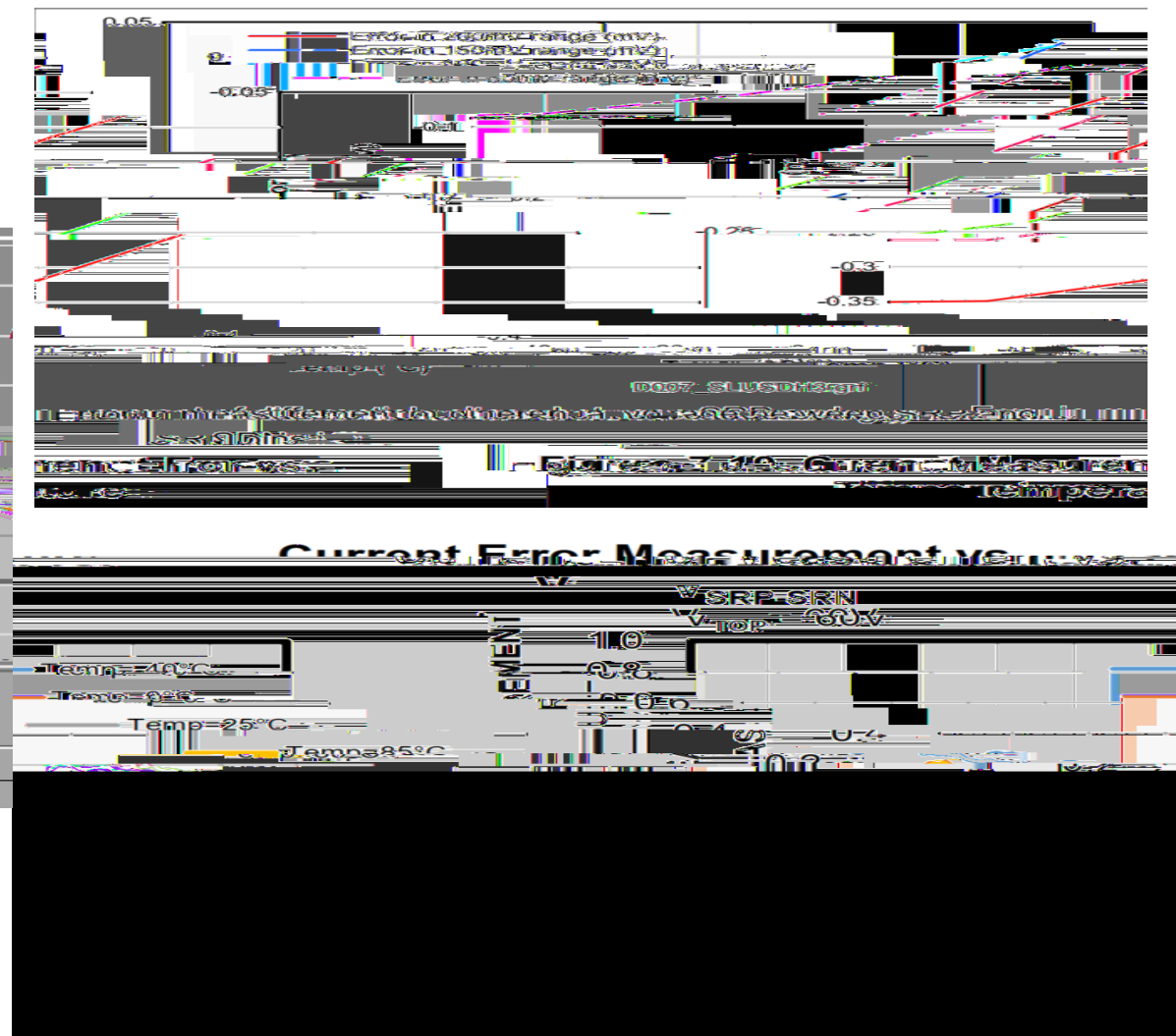
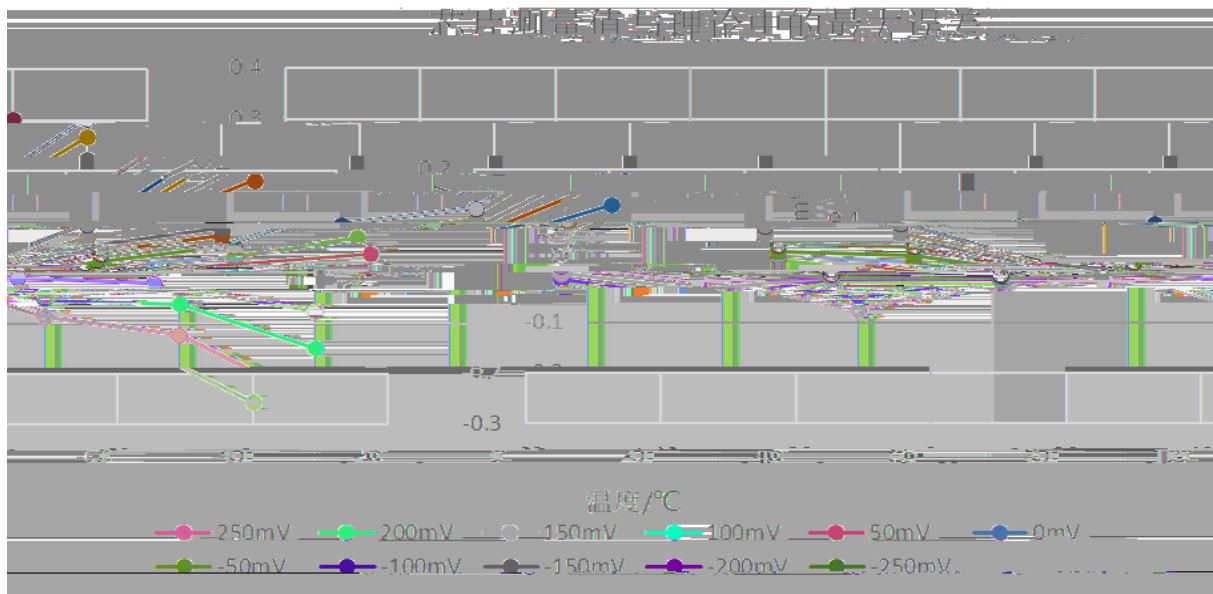
芯片测量值与理论值的最大误差/mV

	-40	-20	0	25	40	60	80
4.3	4.8	1.3	-1.2	-2.4	-2.4	-1.2	3.3
4	4.1	1.2	-1	-2.1	-2.2	-1.9	3.1
3.5	4.6	1.7	-0.6	-1.8	-2	-0.6	2.6
3	3.9	1.5	-2.6	-1.6	-3.1	-0.7	2.1
2.5	4.5	1.8	-0.6	-1.8	-1.7	-0.8	1.2
2	3.3	1.3	-0.5	-1.3	-1.6	-0.9	0.8
1.5	2.5	1.2	-1.9	-1.1	-2.5	-1.2	-0.3
1	3	1.5	0.4	-1.3	-1.5	-1.7	-1.1
0.5	1.9	1	-0.2	-1.1	-1.5	-1.8	-1.9

芯片测量值与理论值的最大误差







Gauge

CPU

32Bit Core, HRC: 4.2MHz, LRC: 65.536KHz

Flash: 64KB, SRAM: 4KB

Peripheral

Supports I2C, HDQ, 2*ADC 16BIT

Algorithm Support: SOH, Qmax@OCV, RSOC

Accuracy :1% RSoC, Current resistor 1mohm,

Power Consumption: Normal <80uA, sleep <20uA;
Deep sleep <10uA × Hibernate mode < 3uA

Authentication: SHA-1\SHA-256 HMAC

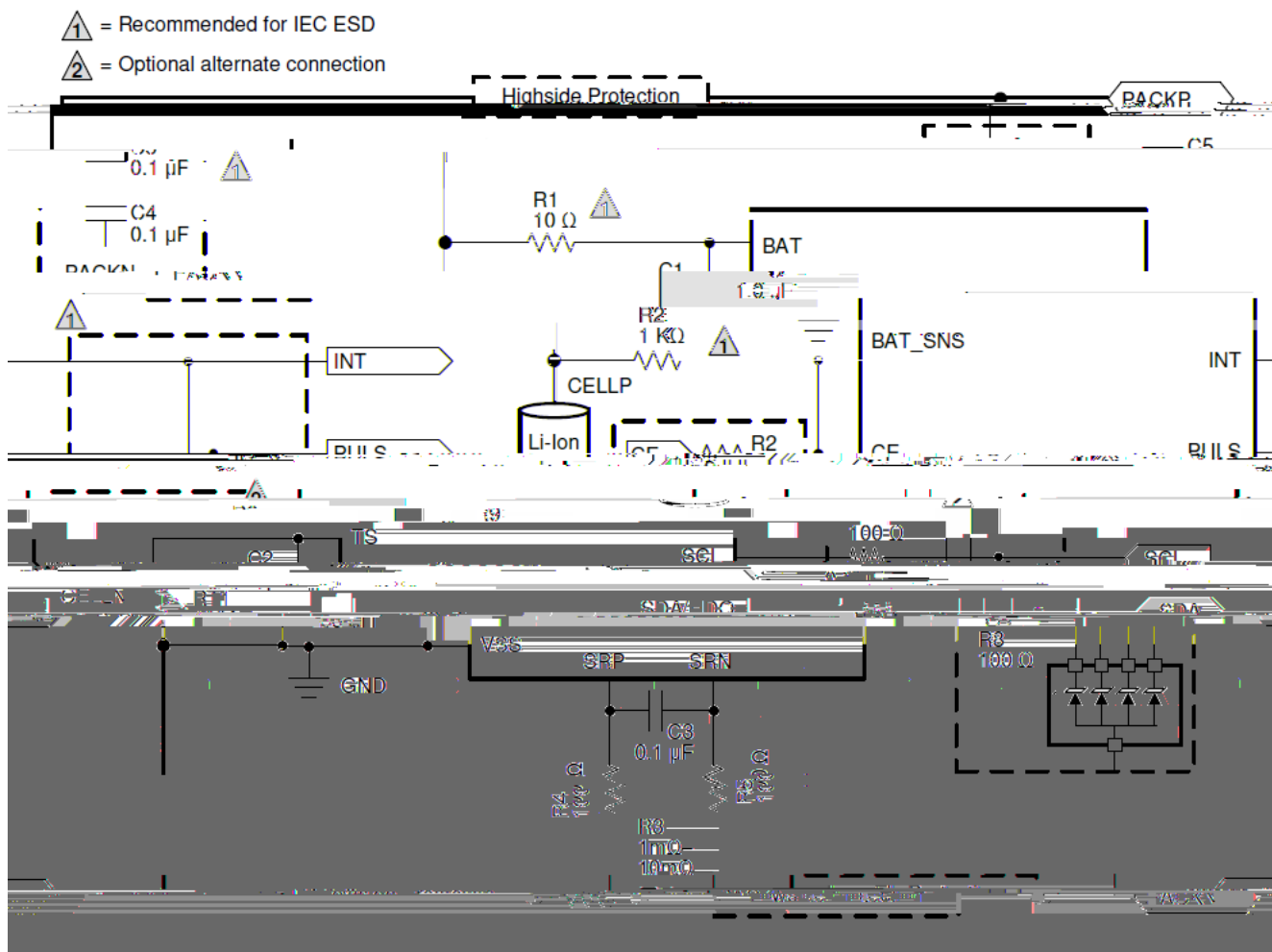
SWD, hardware WDT

Others

Package: CSP12, CSP9 & DFN12

Operation voltage: 2V~5.5V

Operation Temperature: -40 ~+85



Features

Measures Up to 1rUn BattrUnery-22(CeIA)6les -22(i)5(n S)7nerierUn(s)]TÆTQ EMC /Span <</MCID75/Lang (en-US)>BDC 0.000014305 0 960 540 reW* nBT/F11 9.804 Tf1 0 0 1 73.2382.448 Tm0 g

Features

Up to 2 Mbps isolated bidirectional serial data communications

Simple galvanic isolation using capacitors or transformers

Bidirectional interface over a single twisted pair

Supports cable lengths up to 100 meters

Very low EMI susceptibility and emissions

LPCM support for Analog Devices battery monitors

Interrupt output for LPCM system wake-up

Ultralow idle current

Automatic interface wake-up detection

2.7V to 5.5 V isoSPI driver pG[U6V)4(s)-3llyoSPI



× ñ2« [	× ñ » '		+e"• b	Ä+XN¶						
				+e Ø §	Ç §	4ï h H ~	% ¯E–	+e Ø8 > E– -	-	

AFE & Monitor



M' AFJ+XMS 2 j È - x / È W7- \$ H

C# ¾Cµ ~ Ĭ6G Ã2î ö+eGÿN´ `1Ç# Ã ž ¹ ~ ÃQ ÖLö @ Ã1y ° _M™ à ¼ Ò gM0"r

FJ+X,´ Þ × f ~+O 13+5 ¼ /-O § Ö IAR\KEIL

¼ 7,´ é x4× ° __ 1 È Ë SDK È F § f é x

× ñK⁻ O,´ ËCW Añ Ö>15 ¢

... K ÎK⁻ ÈF@!• ¼ ´BMS+O 1 × ñ3+ G

THANKS